

ABSTRACT

Inventor: Andrzej WOZNIAK

Applicant: BULL S.A.

- The invention concerns a method for the automatic recognition of simulation
5 configurations of integrated circuits under design comprising at least two
components connected to one another directly or indirectly, for the functional
verification of the circuits through simulation tests, characterized in that it comprises:
- a step for the acquisition of a simulation configuration by a first manager,
called a “server manager” (14),
 - 10 - a step for the sending of a request by a second manager, called a “client
manager” (11), to the server manager (14),
 - a step for the sending of a response by the server manager (14) to the client
manager (11),
 - a step for the comparison by the client manager (11) of the response with
15 the requirements of the test, followed by a step for the disabling, activation
and/or modification of certain parts of the test by the client manager (11)
in order to adapt the test.

Fig. 2